

Agilent Technologies ESG Family Feature Releases

If your ESG signal generator is older than the most current release date, and it hasn't been upgraded, you are missing recently released features. Updating the firmware in your ESG lets you take advantage of feature enhancements and improvements. Hardware has also been enhanced periodically, to add capability or improve performance, based on customer requests.

New features may be as simple as a free firmware download (www.agilent.com/find/esg) or may also require upgrading ESG hardware (call Agilent Technologies).

NOTE This is a reverse chronological sorting of firmware and hardware feature releases for the ESG family. Use this list to determine the firmware revision and minimum hardware needed to support a specific feature.

Release Date	Feature	Minimum Hardware Required	Firmware Required
10/00	<ul style="list-style-type: none"> • Fully Coded (3GPP 3.2 3-00) W-CDMA personality for user equipment (downlink) and base station (uplink) testing, Opt. 200 (initial release). Based on the following 3GPP Technical Specifications: 3G TS 25.101 V3.2.2 (2000-04) for user equipment testing and 3G TS 24.141 V3.1.0 (2000-03) for base station testing. • Added reverse link capability to the Real-Time cdma2000 personality for fully coded base station testing, Opt. 201 • Added signal-selection capability for certain BNC output connectors to the Real-Time cdma2000 personality, Opt. 201 	ESG-D "B" with Opt. UN8 Rev. C or better (Baseband Fuzzy board E4400-60070 and Flex DG board E4400-60154)	≥ B.03.60
	<ul style="list-style-type: none"> • Enhanced Bluetooth capability, Opt UND. Added sine frequency drift impairment, AWGN, continuous PN9 sequence, and clock/gate signal for BER testing. 	Opt. UND Rev. A or better (Dual Arb board E4400-60069 or E4400-60187)	
9/00	<ul style="list-style-type: none"> • Changed cdma2000 spread rate default to SR1, Opt. 101 	Opt. UND Rev. A or better (Dual Arb board E4400-60069 or E4400-60187)	≥ B.03.51
7/00	<ul style="list-style-type: none"> • Improved ACP, Opt. H99 (Revision B) 	ESG-D/DP "B" with Opt. H99 (Revision B) or better (A9 Output Board, E4400-60188)	≥ B.03.50

Release Date	Feature	Minimum Hardware Required	Firmware Required
6/00	<ul style="list-style-type: none"> • Quick Paging Channel, Opt. 201 • F-FCH/F-SCH Frame Offsets 0 to 15, Opt. 201 • F-FCH/F-SCH Quasi Orthogonal Functions 0 to 3, Opt. 201 • 2-ESG Power Setting UI, Opt. 201 • EDGE Specification Update to Revision 8.3.0-1999, Opt. 202 	ESG-D "B" with Opt. UN8 Rev. C or better (Baseband Fuzzy board E4400-60070 and Flex DG board E4400-60154)	≥ B.03.50
	<ul style="list-style-type: none"> • W-CDMA Specification Update to 3GPP 3.2 3-00, Opt. 100. Added Test Model 4, default offset for PICH to 120, added Paging Indicator Sequence to PICH data selection, unused bits of PICH to DTX (power off), changed PN9 padding to 1s. • Added Table Sorting Feature, Opt. 100, 101, and UN5. • Added Code Domain Power Display, Opt. 100 and 101. • Changed 3GPP Scramble Code Default to 0, Opt. 100 	Opt. UND Rev. A or better (Dual Arb board E4400-60069 or E4400-60187)	
	<ul style="list-style-type: none"> • Remote operation improved with faster firmware operating system 	Hardware independent (firmware runs on LDS 3.11)	
4/00	<ul style="list-style-type: none"> • Real-Time cdma2000 for fully coded IS-2000 receiver testing, Opt. 201 (initial release) • Burst Shape Rise and Fall Editor, Opt. UN8 	ESG-D "B" with Opt. UN8 Rev. C or better (Baseband Fuzzy board E4400-60070 and Flex DG board E4400-60154)	≥ B.03.40
	<ul style="list-style-type: none"> • iDEN Waveforms for MS BER Test, Opt. H60 Rev. D. Additional features are equivalent to Opt. UND Rev D. 	ESG-D "B" with Opt. H60 Rev. D Dual Arb board (E4400-60186) with 50 kHz reconstruction filter instead of 250 kHz filter	
2/00	<ul style="list-style-type: none"> • W-CDMA (3GPP 3.1 12-99) Component Test, Opt. 100 Rev. D. This update to Option 100 implements the 3GPP specification for W-CDMA as defined in December 1999. (Most notably, the chip rate was changed to 3.84 Mcps.) This release also includes W-CDMA (Rev. 1.0-1.2), Opt. 100 Rev. C, as a separate menu selection. 	Opt. UND Rev. A or better (Dual Arb board E4400-60069 or E4400-60187)	≥ B.03.30
	<ul style="list-style-type: none"> • Opt. UND Phase Noise Enhancement, Improves Arb clock phase noise between 1 kHz and 10 kHz. 	Opt. UND Rev. D or better (Dual Arb board E4400-60187)	

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11/99	<ul style="list-style-type: none"> • Bluetooth Personality, Opt. UND 	Opt. UND Rev. A, or better (Dual Arb board E4400-60069)	≥ B.03.20
	<ul style="list-style-type: none"> • Multicarrier NADC, PDC, PHS, GSM, DECT, EDGE, TETRA, PWT, CDPD, APCO25, Opt. UND 	Opt. UND Rev. B or better (Dual Arb board E4400-60187). Rev B board (E4400-60187) will give flatter baseband signals than Rev A board (E4400-60069).	
	<ul style="list-style-type: none"> • Enhanced Clipping, Opts. UND, 100 Rev. C, 101 Rev. B, and UN5 Rev. C 	Opt. UND Rev. A or better (Dual Arb board (E4400-60069)	
	<ul style="list-style-type: none"> • Multicarrier cdma2000, Opt. 101 Rev. B 	Opt. UND Rev. B or better (Dual Arb board E4400-60187). Rev B board (E4400-60187) will give flatter baseband signals than Rev A board (E4400-60069).	
	<ul style="list-style-type: none"> • Multiframe GSM, Opt. UN8 	Opt. UN8 Rev. C or better (Flex DG board E4400-60154)	
	<ul style="list-style-type: none"> • Bit Editor, Opt. UN8 	Opt. UN8 Rev. A or better (Baseband Fuzzy board E4400-60070)	
	<ul style="list-style-type: none"> • Bluetooth, CDPD, APCO25, Opt. UN8 	Opt. UN8 Rev. A or better (Baseband Fuzzy board E4400-60070)	
	<ul style="list-style-type: none"> • Inverted Data Remedy, Opt. UND. Corrects previously reversed I and Q signals for frequencies below 250 MHz. 	Opt. UND Rev. C or better (Dual Arb board E4400-60187)	
10/99	<ul style="list-style-type: none"> • GSM BS Loopback BER, Opt. 300 Rev. A (initial release) 	ESG-D “B” with Opt. 300 Rev. A and: <ul style="list-style-type: none"> • Opt. UN8 Rev D (Baseband Fuzzy board E4400-60070 and Flex DG board E4400-60154) • Opt. UN7 Rev. A (board E4400-60072) • Opt. UNA Not available in ESG-DP Not compatible with Opt. UND	≥ B.03.10

Release Date	Feature	Minimum Hardware Required	Firmware Required
8/99	<ul style="list-style-type: none"> • ESG-AP Models • ESG-DP Models 	<ul style="list-style-type: none"> • E4424B - E4427B • E4434B - E4437B 	≥ B.03.00
	<ul style="list-style-type: none"> • Fast Pulse, Opt. 1E6 (initial release) 	ESG-A or ESG-AP (analog) Not available in ESG-D or ESG-DP (digital)	
	<ul style="list-style-type: none"> • Trigger then Continuous Run, external frame trigger enhancement 	Opt. UN8 Rev. D (Baseband Fuzzy board E4400-60070 and Flex DG board E4400-60154)	
	<ul style="list-style-type: none"> • EDGE Personality, Opt. 202 Rev. A • DECT/PWT Personality Enhancements 	Opt. UN8 Rev. C, or better (Baseband Fuzzy board E4400-60070 and Flex DG board E4400-60154)	
	<ul style="list-style-type: none"> • User File Bit Editor 	Any ESG-D or ESG-DP Series	
	<ul style="list-style-type: none"> • Firmware Utility to Installed Options Revision (e.g. Option UND Hardware Version B) 	ESG-D with Opt. UN3/4, UN8 (Rev. A or higher)	
	<ul style="list-style-type: none"> • Multi-Carrier W-CDMA, Opt. 100 Rev. B enhancement 	Opt. UND Rev. B or greater. Rev. B Dual Arb board (E4400-60187) will give flatter baseband signals than Rev. A (E4400-60069). Rev. B also allows trigger inputs and marker outputs. Arbitrary waveform generator triggers became available in Opt. UND with serial number prefix US3844 or GB3845.	
6/99	<ul style="list-style-type: none"> • EDGE Personality, Opt. H65 (initial release) No longer available - See Opt. 202 8/99. 	Opt. UN8 Rev. C (Baseband Fuzzy board E4400-60070 and Flex DG board E4400-60154) or Opt. UN8 Rev. D (Baseband Fuzzy board E4400-60070 and Flex DG board E4400-60154).	≥ B.02.50
5/99	<ul style="list-style-type: none"> • W-CDMA (ARIB 1.0 - 1.2) Component Test, Opt. 100 Rev. A (initial release based on 3/99 ARIB 1.0 - 1.2 specification). • cdma2000 Component Test, Opt. 101 Rev. A (initial release based on 3/99 cdma2000 Rev. 8 specification) 	ESG "B" with Opt. UND Rev. B or greater. Rev. B board (E4400-60187) will give flatter baseband signals than Rev. A board (E4400-60069). Rev. B board also allows trigger inputs and marker outputs. Arbitrary waveform generator triggers became available in Opt. UND with serial number prefix US3844 or GB3845.	≥ B.02.40
	<ul style="list-style-type: none"> • ESG Board Number and Revision Identifier, firmware utility 	Any ESG series	

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12/98	<ul style="list-style-type: none"> • Multi-Carrier IS-95 CDMA for Component Test, Opt. UN5 Rev. B enhancement • W-CDMA Trial System Multi-Channel Table Editor, Opt H97 enhancement for increased flexibility • Multitone Generation Capability, Opt. UND firmware enhancement • AWGN Generation Capability, Opt. UND firmware enhancement 	ESG "B" with Opt. UND Rev. B or greater. Rev B board (E4400-60187) will give flatter baseband signals than Rev A board (E4400-60069). Rev. B board also allows trigger inputs and marker outputs. Arbitrary waveform generator triggers became available in Opt. UND with serial number prefix US3844 or GB3845.	≥ B.02.24
	<ul style="list-style-type: none"> • Trigger Inputs and Marker Outputs, Opt. UND Rev. B enhancement (initial release) 	Opt. UND Rev. B	
	<ul style="list-style-type: none"> • W-CDMA Trial System Basestation Receiver Test, Opt. H98 enhancement 	ESG-D "B" with Opt. H98	B.02.30 (H98-specific firmware)
8/98	<ul style="list-style-type: none"> • Real-Time Baseband Generator, Opt. UN8 Rev. A (initial release, replaces Opt. UN3 baseband generator). Highly flexible custom modulation for any modulation type/filter/symbol rate. 	ESG-D "B" with Opt. UN8 (Rev. A or higher)	≥ B.02.02
	<ul style="list-style-type: none"> • Opt. UN9 Rev. A (initial release, together with Opt. UN8 replaces Opt. UN4 baseband generator). Adds 7 MBytes RAM to Opt. UN8 for a total of 8 MBytes. Can be added to any configuration with Opt. UN8. 	Opt. UN9	
	<ul style="list-style-type: none"> • Alternate Timeslot Power, Opt. UNA (fast electronic attenuator), for GSM alternate timeslot amplitude switching. 	Opt. UNA	
	<ul style="list-style-type: none"> • iDEN Waveforms for MS BER Test, with Opt. H60 (initial release) 	ESG-D "B" with Opt. H60 Rev. B Dual Arb board (E4400-60186) with 50 kHz reconstruction filter instead of 250 kHz filter	
6/98	<ul style="list-style-type: none"> • W-CDMA Trial System Fully Coded Mobile Receiver Test, Opt. H98 (initial release) 	ESG-D "B" with H98	B.01.10 (H98-specific)

Release Date	Feature	Minimum Hardware Required	Firmware Required
4/98	<ul style="list-style-type: none"> • ESG “B” Revision Platform Roll, includes more firmware memory, bigger power supply to allow for more options in one instrument. Includes the following: 	<ul style="list-style-type: none"> • E4400B/20B/21B/22B (ESG “B”) • E4430B/31B/32B/33B (ESG-D “B”) 	≥ B.01.03
	<ul style="list-style-type: none"> • Arbitrary Waveform Generator, Opt. UND Rev. A (initial release) • Multichannel IS-95 CDMA Personality, Opt. UN5 Rev. A (initial release) • W-CDMA Trial System Partially Coded Component Test, Opt. H97 (initial release) 	Opt. UND Rev. A board (E4400-60069)	
	<ul style="list-style-type: none"> • Bit Error Rate Test, Opt. UN7 (initial release) 	Opt. UN7 Rev. A board (E4400-60072)	
	<ul style="list-style-type: none"> • Improved ACP, Opt. H99 (initial release) 	Opt. H99 Rev. A board (E4400-60155)	
	<ul style="list-style-type: none"> • High Power with Mechanical Attenuator, Opt. UNB (initial release) 	Opt. UNB	
	<ul style="list-style-type: none"> • I/Q Quadrature Impairments, firmware enhancement • Frequency Channels for TDMA Standards, firmware enhancement • Secondary TDMA Framing, firmware enhancement 	ESG-D “A” or “B”	≥ B.01.03 for “B” version, B.01.03 only for “A” version
11/97	<ul style="list-style-type: none"> • Hardware PN Generator, Opt. UN3/4 firmware enhancement • Frame Trigger for PHS and PDC, Opt. UN3/4 firmware enhancement 	ESG-D “A” with Opt. UN3/4	A.01.20 to B.02.03
5/97	<ul style="list-style-type: none"> • Opt. H03 Enhancements 	None	A.01.20 through A.02.44
	<ul style="list-style-type: none"> • Single-Channel Partially Coded CDMA, Opt. H03 	ESG-D “A” with Opt. H03	Included
3/97	<ul style="list-style-type: none"> • New Baseband Generator, Opt. UN3/4 (initial release, replaces Opt. 1EH). Adds: <ul style="list-style-type: none"> • DECT and TETRA Personalities • Modulation Filter Choice in All TDMA Personalities • Symbol Rate Choice in All TDMA Personalities 	ESG-D “A” with Opt. UN3/4	A.01.10 through B.01.03
10/96	<ul style="list-style-type: none"> • Firmware Enhancements 	ESG-D “A” with Opt. 1EH	≥ A.01.10
8/96	<ul style="list-style-type: none"> • ESG-A Models, (initial release) • ESG-D Models, (initial release) 	<ul style="list-style-type: none"> • E4400A/20A/21A/22A (ESG “A”) • E4430A/31A/32A/33A (ESG-D “A”) 	≥ A.01.00
	<ul style="list-style-type: none"> • Baseband Generator with GSM, NADC, PDC, and PHS TDMA Personalities, Opt. 1EH (initial release) 	Opt. 1EH	

ESG-D Baseband Generator Evolution (Oldest to Newest)

TDMA Baseband Generators

1EH - Original, single-board baseband generator. Provided NADC, GSM, PDC, and PHS personalities. Limited flexibility to change data rates and filters.

UN3/4 - Two-board enhanced baseband generator. Provided NADC, GSM, PDC, PHS, and new DECT and TETRA personalities. More flexibility to change data rates and filters over a limited range.

UN8/9 Real-Time Baseband Generator

Rev. A - New baseband board (first Fuzzy board) with custom ASIC. With UN3/4 data generator board allows any I/Q modulation type, data rates to 12.5 Msps, and create-your-own FIR filter.

Rev. B - Enhanced baseband board (second Fuzzy board). Provides polarity change to DATA and DATA CLOCK inputs.

Rev. C - New data generator board. With first Fuzzy board provides DSP, FPGA, and RAM for dramatic increase in channel coding complexity.

Rev. D - New data generator board. With second Fuzzy board provides combined enhancements from previous revisions B and C.

		Data Generator Board	
		Original	New
Baseband Fuzzy Board	Original	Rev. A	Rev. C
	New	Rev. B	Rev. D

Option UND Dual Arbitrary Waveform Generator Evolution

Rev. A - Initial release of dual arbitrary waveform generator board.

Rev. B - Added triggers, marker hardware, and flattened the reconstruction filter frequency response.

Rev. C - Hardware modified to remedy reversed I and Q signals for frequencies below 250 MHz.

Rev. D - Hardware modified to improve Arb clock phase noise between 1 kHz and 10 kHz.